

# Abstracts

## A Ku-band 6-bit digital attenuator with integrated serial to parallel converter

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*J.C. Sarkissian, M. Delmond, E. LaPorte, E. Rogeaux and R. Soulard. "A Ku-band 6-bit digital attenuator with integrated serial to parallel converter." 1999 MTT-S International Microwave Symposium Digest 99.1 (1999 Vol. I and IV [MWSYM]): 1915-1918*

A monolithic Ku-band 6-bit digital attenuator with on chip serial to parallel converter has been developed. The integration of a digital part reduces the bias circuitry and opens the way to new circuits combining digital and analog functions. The circuit has a total size of  $3.0/\text{spl} \times 2.0/\text{mm}$  for a DC power consumption less than 20 mW. The technology used is a  $0.2/\text{spl} \mu\text{m}$  PHEMT process including enhancement and depletion mode devices.

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